

Docket Number
36856.1464

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kazuhide KUDO et al. Application No.: 10/599,368 Confirmation No.: 7685 Filing or 371(c) Date: September 27, 2006 Title: METHOD FOR MANUFACTURING ELECTRONIC COMPONENTS, MOTHER SUBSTRATE, AND ELECTRONIC COMPONENT	Art Unit: 2822 Examiner: M. Wilczewski
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INFORMATION DISCLOSURE STATEMENT COVER LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

For the Examiner's convenience, Applicant has enclosed an English translation of the Official Communication issued in the corresponding non-U.S. application. Applicant has had this translation prepared by a professional translation service and Applicant presently has no reason to doubt that the translation is accurate. However, Applicant has not independently verified the accuracy of such translation, and accordingly, submission of the same should not be taken as a binding admission by Applicant that the translation is accurate.

The statement is not a representation that all of the information cited is necessarily effective as prior art against the application.

Applicant respectfully requests that the cited document(s) be made of record in the subject application.

Respectfully submitted,

Dated: July 30, 2009

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